

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings of claims in the application.

**Listing of Claims:**

1. (Currently Amended) An integrated circuit device, comprising:  
a controller;~~and~~  
a serial trace port, wherein the serial trace port provides controller trace data and wherein the controller trace data is provided external to the integrated circuit device using a differential serial channel; ~~and~~  
a second controller, wherein the serial trace port also provides controller trace data of the second controller,  
wherein the serial trace port receives a reference clock signal and provides a clock signal to each of the controller and second controller.
2. (Original) The device of Claim 1, wherein the differential serial channel transmits data, control and timing information in a serial stream.
3. (Cancelled)
4. (Cancelled)
5. (Currently Amended) The device of Claim ~~[[3]]~~1, further comprising:  
a trace buffer operatively coupled to the controller and the second controller; and  
a serializer, operatively coupled between the differential serial channel and the trace buffer, which converts a parallel data stream from the trace buffer to a serial data stream for the differential serial channel.
6. (Original) The device of Claim 1, further comprising:  
a trace buffer operatively coupled to the controller;  
a serializer, operatively coupled between the differential serial channel and the trace buffer, which converts a parallel data stream from the trace buffer to a serial data stream for the differential serial channel.

7. (Original) The device of Claim 6, wherein the parallel data stream comprises compressed data.

8. (Original) The device of Claim 5, wherein the serial trace port also provides a serializer clock signal to the serializer.

9-23. (Cancelled)